CLASSIFICATION	PRODUCT SPECI	FICATION	No. DS-1315-2400-1	02	REV. 1.04
SUBJECT	CLASS 1 or 2 BLUETOOTH	MODULE	PAGE	1 of 4	.1
CUSTOMER'S CODE PAN1315	PANASONIC'S ENW89818C2		DATE	02.07.20)11
S	Specification	for Proc	luction		
Applicant / Manuf Hardware	facturer Panasonic E Zeppelinstra 21337 Lünel Germany		urope GmbH		
Applicant / Manuf Software	facturer Not applicat	le			
Software Version	Not applicat	le			
Contents	Approval for	Mass Production			
Customer					
company and that acknowledges that defects due to the Panasonic reject Panasonic disclai • the use of installation • deviation or • improper us	document, you acknowledge at you understand and acc at engineering samples m eir development status. Is any liability or product ms liability for damages cau the engineering sample of or integration in an other pro- r lapse in function of engineer se of engineering samples. aimes any liability for cons PROVED:	ept the validity of ay deviate from thi warranty for enginesed by ther than for evaluation oduct to be sold by ering sample,	the contents here is specification ar neering samples nation purposes, Customer,	rein. Cua nd may c . In par particula	stomer contain ticular,
DATE:	NAME:	SIGNATURI	3:		
Modu PANASONIC EL	Y PRODUCTS DIVISION le Business .ECTRONIC DEVICES OPE GmbH	APPROVED genehmigt	CHECKED geprüft		GNED stellt

CLASSIF	ICATIO	N PR	ODUCT SPECIFICATION	No. DS-1315-2400-10)2	REV. 1.04
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CLASSIFICATION	PRODUCT SPECIFICATION	No. DS-1315-2400-102	REV. 1.04
SUBJECT CLASS 1	or 2 BLUETOOTH MODULE	PAGE 4 of	41
CUSTOMER'S CODE PAN1315	PANASONIC'S CODE ENW89818C2JF	DATE 02.07.2	
 SCOPE OF THIS DO This master product specifi output power register value different versions. Different Ordering Information). The Bluetooth chip used is (http://focus.ti.com/pdfs/wtbu/v KEY FEATURES Bluetooth specification Surface mount type of Up to 10.5dBm Tx por High sensitivity (-93) Texas Instrument's O Fast Connection Setting Supports convenient or connect to DC/DO Internal crystal oscill Built-in shielding to b Full Bluetooth data restructed and the Support for Bluetooth Support for Bluetooth Support for Support for PCM Interface Masta Codecs and CVSD tr Full 8- to 128-bit end UART, I2C and PCM IO operating voltage 3 Channel ADC and Bluetooth profiles su website for a listing of 	ENW89818C2JF CUMENT cation applies to Panasonic's HCI, CI s), Bluetooth® ¹ module, series numb versions of the PAN1315 are availab the CC2560 from Texas Instruments cc2560_slyt377.pdf). on v2.1 + EDR (Enhanced Data Rate 6.5 x 9.0 x 1.8 mm ³ ower (typical) with transmit power co dBm typ.) CC2560 BlueLink 7.0 inside up c direct connection to battery (2.2-4.8 C (1.7-1.98 V) for improved power efficient ator (26MHz) be compliant to FCC ate up to 2178kbps asymmetric h power saving modes (Sniff, Hold) -power modes (deep sleep and power ultra-low-power mode. Standby with ter / Slave supporting 13 or 16 bit ranscoders on up to 3 SCO channels cryption Interface = 1.8 V nominal	ass 1 or 2 (depending on ber: PAN1315 and their ble (refer to chapter 23 e) ntrol V), iciency er down) Battery-Backup linear, 8 bit µ-law or A-la	aw

¹ Bluetooth is a registered trademark of the Bluetooth Special Interest Group.

		No.	REV.
CLASSIFICATION PF	RODUCT SPECIFICATION	DS-1315-2400-102	1.04
CLASS 1 or	2 BLUETOOTH MODULE	PAGE 5 of 4	41
CUSTOMER'S CODE PAN1315	PANASONIC'S CODE ENW89818C2JF	DATE 02.07.2	011
3. APPLICATIONS FOR T	HE MODULE		
 All Embedded Wireless Applie Access Points Industrial Control Medical Scanners Wireless Sensors Low Power 4. DESCRIPTION FOR THE	 Cable Replace Personal Digit Access Points Computers an Printer Adapte Printers 	tal Assistants (PDAs) s nd Peripherals	
functionality into various electron Communication between the restriction betw	e Class 1 or 2 HCI module for impler ronic devices. A block diagram can be nodule and the host controller is can based on TI's MSP430 platform, us y Mindtree Ltd. rocessors is avaialbe from other Par s office for further details on addition modules or write an e-mail to wirele	be found in chapter 7. rried out via UART. sing TI's part number nasonic software partners nal options and services, I	

CLASSIFICATION	PRO	DUCT SPECIFICATION	No. DS-1315-240	0-102	REV. 1.04
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CUSTOMER'S CODE PAN1315		PANASONIC'S CODE ENW89818C2JF	DATE	02.07.20)11
5. DETAILED DESC 5.1. PAN1315 TERM		AYOUT 9,00 mm 19 17 15 13 20 18 16 14 21 Pad = 24 x 0.60mm x 0.60mm 11 Module Height 1.8 mm 23 Top View 9 24 2 4 6	- 10		
Please note t	that the	ES samples with HW Revision] 01 have 0.5m	nm pads!	

Please note that the ES samples with HW Revision 01 have 0.5mm pads!

No	Pin Name	Pull at Reset	Def. Dir. ²	I/O Type ³	Description of Options (Common)	
1	GND				Connect to Ground	
2	TX_DBG	PU	0	2 mA	Logger output	
3	HCI_CTS	PU	1	8 mA	HCI UART clear-to-send.	
4	HCI_RTS	PU	0	8 mA	HCI UART request-to-send.	
5	HCI_RX	PU	1	8 mA	HCI UART data receive	
6	HCI_TX	PU	0	8 mA	HCI UART data transmit	
7	AUD_FSYNC	PD	10	4 mA	PCM frame synch. (NC if not used)	Fail safe ⁴
8	SLOW_CLK_IN		I		32.768-kHz clock in	Fail safe
9	NC		10		Not connected	
10	MLDO_OUT		0		Main LDO output (1.8 V nom.)	
11	CL1.5_LDO_IN		1		PA LDO input	
12	GND				Connect to Ground	
13	RF		10		Bluetooth RF IO	
14	GND				Connect to Ground	
15	MLDO_IN		1		Main LDO input	
16	nSHUTD	PD	1		Shutdown input (active low).	
17	AUD_OUT	PD	0	4 mA	PCM data output. (NC if not used)	Fail safe
18	AUD_IN	PD	1	4 mA	PCM data input. (NC if not used)	Fail safe
19	AUD_CLK	PD	10	HY, 4 mA	PCM clock. (NC if not used)	Fail safe
20	GND				Connect to Ground	
21	NC				EEPROM I ² C SDA (Internal)	
22	VDD_IO		PI		I/O power supply 1.8 V Nom	
23	NC				EEPROM I ² C SCL (Internal)	
24	NC		10		Not connected	

 2 I = input; O = output; IO = bidirectional; P = power; PU = pulled up; PD = pulled down

³ I/O Type: Digital I/O cells. HY = input hysteresis, current = typ. output current

⁴ No signals are allowed on the IO pins if no VDD_IO (Pin 22) power supplied, except pin 7, 8, 17-20.

CLASSIFICATION	PRODUCT	SPECIFICATION	No. DS-1315-2400-102	REV. 1.04
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5.2. PIN DESCRIPTION

Pin Name	No	ESD ⁵ (V)	Pull at Reset	Def. Dir. ⁶	I/O Type ⁷	Description of Options
Bluetooth IO SIG	NALS		r	1	F	
HCI_RX	5	750	PU	I	8 mA	HCI UART data receive
HCI_TX	6	750	PU	0	8 mA	HCI UART data transmit
HCI_RTS	4	750	PU	0	8 mA	HCI UART request-to-send.
HCI_CTS	3	750	PU	I	8 mA	HCI UART clear-to-send.
AUD_FYSNC	7	500	PD	10	4 mA	PCM frame synch (NC if not used) Fail safe
AUD_CLK	19	500	PD	10	HY, 4 mS	PCM clock (NC if not used) Fail safe
AUD_IN	18	500	PD	1	4 mA	PCM data input (NC if not used) Fail safe
AUD_OUT	17	500	PD	0	4 mA	PCM data output (NC if not used) Fail safe
TV DDO		1000				Logger output
TX_DBG	2	1000	PU	0	2 mA	OPTION: nTX_DBG – logger out (low = 1)
CLOCK SIGNALS	S					
SLOW_CLK_IN	8	1000		I		32.768-kHz clock in Fail safe
Bluetooth ANALC	OG SIG	NALS				
RF	13	1000		10		Bluetooth RF IO
nSHUTD	16	1000	PD	1		Shutdown input (active low).
Bluetooth POWE	r and	GND SI	GNALS			
VDD_IO	22	1000		ΡI		I/O power supply 1.8 V Nom
MLDO_IN	15	1000		I		Main LDO input Connect directly to battery or to a pre-regulated 1.8-V supply
MLDO_OUT	10	1000		0		Main LDO output (1.8 V nom.)
CL1.5_LDO_IN	11	1000		I		PA LDO input Connect directly to battery or to a pre-regulated 1.8-V supply
GND	1			Р		Connect to Ground
GND	12			P		Connect to Ground
GND	14	1		P		Connect to Ground
GND	20			Р		Connect to Ground
EEPROM IO SIG	NALS	(EEPRO	M is optiona	al in PA	N13x product	t line)
NC	23	1000	PU/PD	I	HY, 4mA	EEPROM I ² C SCL (Internal)
NC	21	1000	PU/PD	ю	HY, 4mA	EEPROM I ² C IRQ (Internal)

Remark:

HCI_CTS is an input signal to the CC2560 device:

- When HCI_CTS is low, then CC2560 is allowed to send data to Host device.

- When HCI_CTS is high, then CC2560 is not allowed to send data to Host device.

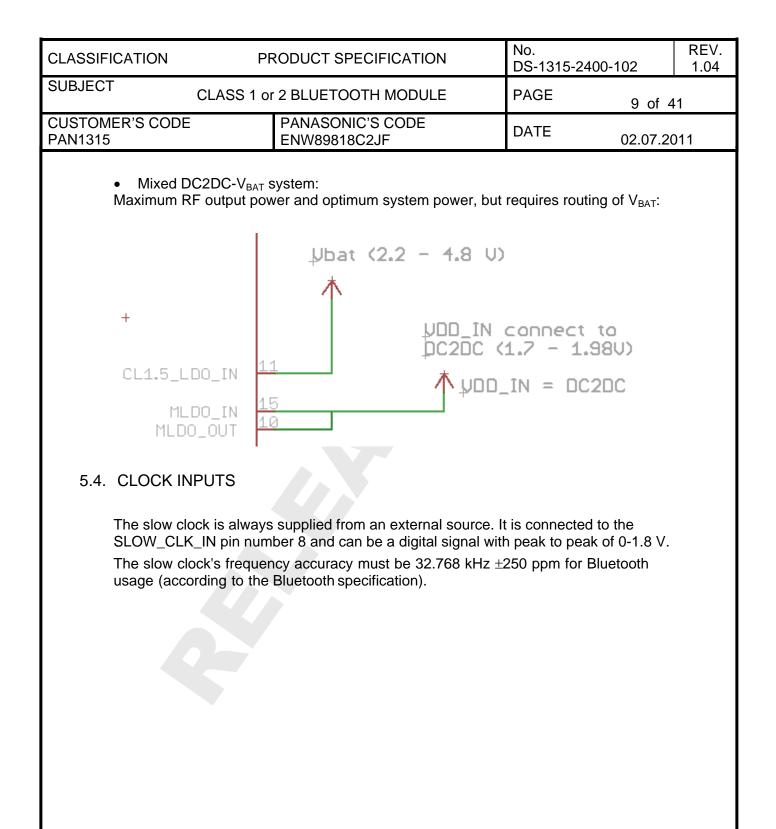
⁶ I = input; O = output; IO = bidirectional; P = power; PU = pulled up; PD = pulled down

⁷ I/O Type: Digital I/O cells. HY = input hysteresis, current = typ output current

⁵ ESD: Human Body Model (HBM). JEDEC 22-A114

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CUSTOMER'S COD PAN1315	E	PANASC ENW898	NIC'S CODE 18C2JF		DATE	02.07.2	2011
5.3. DEVICE	POWER SU	IPPLY					
power buc operated o	dget such as o devices. One	cellular pho of the mair	ution is intended ones, headsets, n differentiators as little current a	hand-held of the PAN	PC's and c 1315 is its	other battery-	
• Main p	ower supply fo	or the Blue	kinds of power tooth - VDD_IN ring - VDD_IO				
The PAN1	315 includes : 315 can be co	several on-	-chip voltage re- ither directly to	0			
● Full-V _B	three ways to BAT system: RF output pov		wer: t optimum syste	em power:			
Μ	LDO_IN LDO_IN DO_OUT	L1 L5	UD_IN Ubat (2.)	connec 2 - 1 .8 : Conne			
	C2DC system: output power,		um system pow	er:			
+	1	1	μοο_ΙΝ « ρc2οc «1	tonnec 1.7 – 1	t to 98V)		
М	LDO_IN ILDO_IN DO_OUT	-5 -0	1				

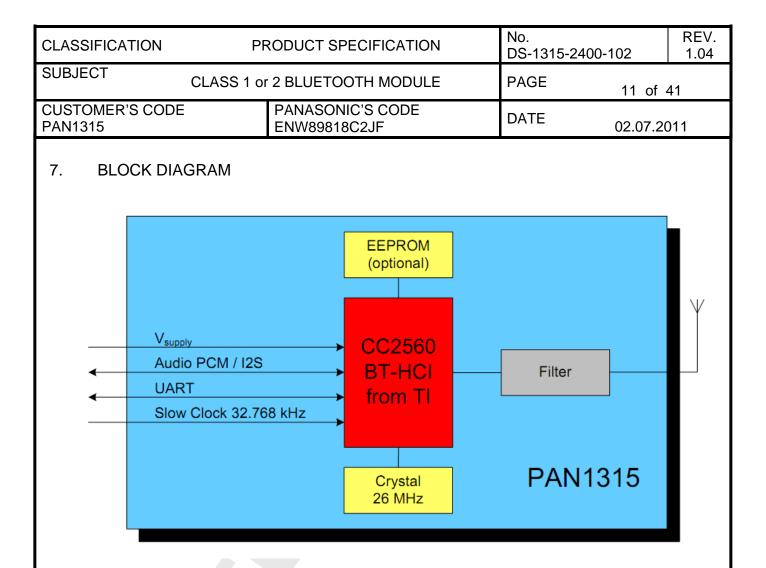
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6. BLUETOOTH FEATURES

- Support of Bluetooth2.1+EDR (Lisbon Release) up to HCI level.
- Very fast AFH algorithm for both ACL and eSCO.
- Supports typically 4 dBm Class 2 TX power w/o external PA, improving Bluetooth link robustness. Adjusting the host settings, the TX power can be increased to 10 dBm. However it is important, that the national regulations and Bluetooth specification are met.
- Digital Radio Processor (DRP) single-ended 50 ohm.
- Internal temperature detection and compensation ensures minimal variation in the RF performance over temperature.
- Flexible PCM and I2S digital audio/voice interfaces: Full flexibility of data-format (Linear, a-Law, μ-Law), data-width, data order, sampling and slot positioning, master/slave modes, high clock rates up to 15 MHz for slave mode (or 4.096 MHz for Master Mode). Lost packet concealment for improved audio.
- Proprietary low-power scan method for page and inquiry scans, achieves page and inquiry scans at 1/3rd normal power.



The EEPROM could be used to store some calibration value for the 26 MHz crystal at room temperature, so the max. operating temperature could be increased to +85°C.

In addition, the start up time could be decrease, as the patch can be loaded from the EEPROM,. For details please contact your local sales office.

Note: The Slow Clock 32.768 kHz is mandatory, otherwise the module does not start up, please refer to chapter 5.4.

Note: The IO are 1.8V driven and might need external level shifter and LDO. The MLDO_OUT PIN can not be used as reference due to RF internal connection.

PAN1315 8. TI	CLA ER'S CODE	SS 1 or 2 BLUETOOTH MODULE	PAGE		
PAN1315 8. TI Meas	ER'S CODE		TROE	12 of	41
Meas		PANASONIC'S CODE ENW89818C2JF	DATE	02.07.20	
	EST CONDITIO	NS			
		e made under room temperature ar	nd humidity unless ot	herwise	
9. G	ENERAL DEVIC	CE REQUIREMENTS AND OPE	RATION		
Humi SW-P	dity	25 ± 10°C 40 to 85%RH V2.30 3.3V			
All sp	ecifications are o	ver temperature and process, unle	ss indicated otherwis	e.	
	Over operating fre	XIMUM RATINGS ee-air temperature range (unless o	therwise noted).		
	Over operating free Note All pa			vise:	
	Over operating free Note All pa	ee-air temperature range (unless o		vise:	
(Over operating fre Note All pa VDD	ee-air temperature range (unless of arameters are measured as follows _IN 8 = 3.3 V, VDD_IO = 1.8 V.	unless stated otherv		
(Over operating free Note All pa VDD No See ⁹ Ratings Over Operating	ee-air temperature range (unless o	unless stated otherv	Unit	
(Over operating free Note All pa VDD No See ⁹ Ratings Over Operating	ee-air temperature range (unless of arameters are measured as follows $_{IN}^{8} = 3.3 \text{ V}, \text{VDD}_{IO} = 1.8 \text{ V}.$	unless stated otherv	Unit 5.5 V ¹⁰	
(Over operating free Note All pa VDD No See ⁹ Ratings Over Operating 1 VDD_IN	ee-air temperature range (unless of arameters are measured as follows _IN 8 = 3.3 V, VDD_IO = 1.8 V.	unless stated otherv Value	Unit 5.5 V ¹⁰ 2.145 V	
(Note Note All pa VDD No See ⁹ Ratings Over Operating 1 VDD_IN 2 VDDIO_1.8V 3 Input voltage to R	ee-air temperature range (unless of arameters are measured as follows _IN 8 = 3.3 V, VDD_IO = 1.8 V.	vunless stated otherv Value –0.5 to –0.5 to	Unit 5.5 V ¹⁰ 2.145 V 2.1 V	
	Note Note All pa VDD No See ⁹ Ratings Over Operating 1 VDD_IN 2 VDDIO_1.8V 3 Input voltage to R	ee-air temperature range (unless o arameters are measured as follows _IN ⁸ = 3.3 V, VDD_IO = 1.8 V. g Free-Air Temperature Range Supply voltage range F (Pin 13) t temperature range	vunless stated otherv Value -0.5 to -0.5 to -0.5 to	Unit 5.5 V ¹⁰ 2.145 V 2.1 V 70 °C	
	Note Note All pa VDD No See ⁹ Ratings Over Operating 1 VDD_IN 2 VDDIO_18V 3 Input voltage to R 4 Operating ambier	ee-air temperature range (unless of arameters are measured as follows _IN 8 = 3.3 V, VDD_IO = 1.8 V. g Free-Air Temperature Range Supply voltage range F (Pin 13) t temperature range ure range	value Value -0.5 to -0.5 to -20 to	Unit 5.5 V ¹⁰ 2.145 V 2.1 V 70 °C	

¹⁰ Maximum allowed depends on accumulated time at that voltage: VDD_IN is defined in Reference schematics. When DC2DC supply is used, maximum voltage into MLDO_OUT and LDO_IN = 2.145 V.

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9.2. RECOMMENDED OPERATING CONDITIONS

No	Rating	Condition	Symbol	Min	Max	Unit
1	Power supply voltage ¹¹		VDD_IN	1.7	4.8	V
2	IO power supply voltage		VDD_IO	1.62	1.92	V
3	High-level input voltage	Default	V _{IH}	0.65 x VDD_IO	VDD_IO	V
4	Low-level input voltage	Default	V _{IL}	0	0.35 x VDD_IO	V
5	IO Input rise/fall times, 10% to 90% 12		Tr/Tf	1	10	ns
		0 to 0.1 MHz			60	
		0.1 to 0.5 MHz			50	
6	Maximum ripple on VDD_IN (Sine wave) for 1.8 V (DC2DC) mode	0.5 to 2.5 MHz			30	mVp-p
		2.5 to 3.0 MHz			15	
		> 3.0 MHz			5	
7	Voltage dips on VDD_IN (V _{BAT}) (duration = 577 μ s to 2.31 ms, period = 4.6 ms)				400	mV
8	Maximum ambient operating temperature ¹³				70	°C

9.3. CURRENT CONSUMPTION

No	Characteristics	Min 25°C	Typ 25°C	Max 25°C	Min -20°C	Typ -20°C	Max -20°C	Min +70°C	Typ +70°C	Max +70°C	Unit
1	Current consumption in shutdown mode ¹⁴		1	3						7	μΑ
2	Current consumption in deep sleep mode ¹⁵		40	105						700	μA
3	Total IO current consumption for active mode			1			1			1	mA
4	Current consumption during transmit DH5 full throughput		40								mA

¹¹ Excluding 1.98 < VDD_IN < 2.2 V range – not allowed.

- ¹² Asynchronous mode.
- ¹³ The device can be reliably operated for 7 years at T_{ambient} of 70°C, assuming 25% active mode and 75% sleep mode (15,400 cumulative active power-on hours).
- ¹⁴ Vbat + Vio
- ¹⁵ Vbat + Vio + Vsd (shutdown)

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9.4. GENERAL ELECTRICAL CHARACTERISTICS

No	Rating			Condition	Min	Max	Value
1	High lovel outp	High-level output voltage, V _{OH}			0.8 x VDD_IO	VDD_IO	V
1				at 0.1 mA	VDD_IO - 0.2	VDD_IO	V
2				at 2/4/8 mA	0	0.2 x VDD_IO	V
2	Low-level output voltage, V _{OL}			at 0.1 mA	0	0.2	V
3				Resistance	1		MΩ
3	IO input impeda	ance		Capacitance		5	pF
4	Output rise/fall	times,10% to 909	% (Digital pins)	C _L = 20 pF		10	Ns
		TX_DBG,	PU	typ = 6.5	3.5	9.7	
F	5 IO pull currents	PCM bus	PD	typ = 27	9.5	55	μA
5			PU	typ = 100	100	300	
		All others PD		typ = 100	100	360	μA

9.5. NSHUTD REQUIREMENTS

No	Parameter	Symbol	Min	Max	Unit
1	Operation mode level ¹⁶	V _{IH}	1.42	1.98	V
2	Shutdown mode level	VIL	0	0.4	V
3	Minimum time for nSHUT_DOWN low to reset the device		5		ms
4	Rise/fall times	Tr/Tf		20	μs

9.6. EXTERNAL DIGITAL SLOW CLOCK REQUIREMENTS (-20°C TO +70°C)

No	Characteristics	Condition	Symbol	Min	Тур	Max	Unit
1	Input slow clock frequency				32768		Hz
2	Input slow clock accuracy (Initial + temp + aging)	Bluetooth				±250	Ppm
3	Input transition time Tr/Tf – 10% to 90%		Tr/Tf			100	Ns
4	Frequency input duty cycle			15%	50%	85%	
5	Phase noise	at 1 kHz				-125	dBc/Hz
6	Jitter	Integrated over 300 to 15000 Hz				1	Hz
7	Slow clock input voltage	Square wave, DC coupled	VIH	0.65 x VDD_IO		VDD_IO	V peak
7	limits	Square wave, DC coupled	VIL	0		0.35 x VDD_IO	vpeak
8	Input impedance			1			MΩ
9	Input capacitance					5	pF

¹⁶ Internal pull down retains shut down mode when no external signal is applied to this pin.

-						
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10.	HOST CONTR	OLLER II	NTERFACE			
	(HCI) transpo	rt layer. Th	es one UART module dedicated to le HCI interface transports comma een the Bluetooth device and its ho	inds, events, A	CL, and	;

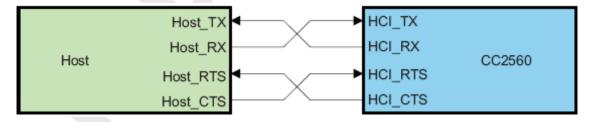
The UART module supports H4 (4-wires) protocol with maximum baud rate of 4 Mbps for all fast clock frequencies.

After power up the baud rate is set for 115.2 kbps, irrespective of fast clock frequency. The baud rate can thereafter be changed with a vendor specific command. The CC2560 responds with a Command Complete Event (still at 115.2 kbps), after which the baud rate change takes place. HCI hardware includes the following features:

- Receiver detection of break, idle, framing, FIFO overflow, and parity error conditions
- Transmitter underflow detection
- CTS/RTS hardware flow control

The interface includes four signals: TXD, RXD, CTS, and RTS. Flow control between the host and the CC2560 is byte-wise by hardware.

Flow control is obtained by the following:



When the UART RX buffer of the CC2560 passes the "flow control" threshold, it will set the UART_RTS signal high to stop transmission from the host.

When the UART_CTS signal is set high, the CC2560 will stop its transmission on the interface. In case HCI_CTS is set high in the middle of transmitting a byte, the CC2560 will finish transmitting the byte and stop the transmission.

11. AUDIO/VOICE CODEC INTERFACE

The codec interface is a fully-dedicated programmable serial port that provides the logic to interface to several kinds of PCM or I2S codecs. PAN1315 supports all voice coding schemes required by Bluetooth specification – Log PCM (A-Law or μ -Law) and Linear (CVSD). In addition, module also supports transparent scheme:

- Two voice channels
- Master / slave modes
- µ-Law, A-Law, Linear, Transparent coding schemes
- Long and short frames
- Different data sizes, order, and positions.
- High rate PCM interface for EDR

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Enlarged interfaPCM bus sharin	ce options to support a v g	wider variety of cod	ecs	
11.1. PCM HARDWA The PCM interfac following four line	e is one implementation	of the codec interfa	ace. It contains the	
•	able direction (input or c			
 Frame Sync—co Data In—Input 	onfigurable direction (inp	out or output)		
Data M—Mput Data Out—Outp	ut/3-state			
The Bluetooth de clock and the fran		where it receives t		
above 12 MHz, th		size is 32 bits. For n	supported. At clock rates naster mode, the CC2560 Iz.	
11.2. DATA FORMA	г			
	s fully configurable:			
	640 bits when using 1		ts, when working with two length can be set)
	n within a frame is also endently (relative to the e	•	1 clock (bit) resolution an Sync signal) for each	d
Data_In can start	with the MSB while Data urable. The inverse bit o	a_Out starts with LS	ependently. For example; SB. Each channel is rst) is supported only for	
 It is not necessa 	ary for the data in and da	ta out size to be the	e same length.	
also be set for pe	rmanent high-Z, irrespec	ctive of data out. Th	data words. Data_Out car is allows the CC2560 to b Data Out is configured as	
		•	where the PCM clock car after all data has been	ו
The CC2560 sup	ports frame idle periods	both as master and	I slave of the PCM bus.	
When CC2560 is are two configura		ice, the frame idle p	period is configurable. The	ere
• Clk_Idle_Start -	Indicates the number of		from the beginning of the art clock cycles, the clock	
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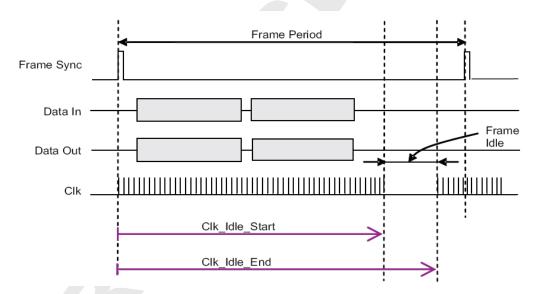
CLASSIFICATION	PR	ODUCT SPECIFICATION	No. DS-1315-2	400-102	REV. 1.04
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• Clk_Idle_End – Indicates the time from the beginning of the frame till the end of the idle period. This time is given in multiples of PCM clock periods.

The delta between Clk_Idle_Start and Clk_Idle_End is the clock idle period.

For example, for PCM clock rate = 1 MHz, frame sync period = 10 kHz, Clk_ldle_Start = 60, Clk_ldle_End = 90.

Between each two frame syncs there are 70 clock cycles (instead of 100). The clock idle period starts 60 clock cycles after the beginning of the frame, and lasts 90 - 60 = 30 clock cycles. This means that the idle period ends 100 - 90 = 10 clock cycles before the end of the frame. The data transmission must end prior to the beginning of the idle period.

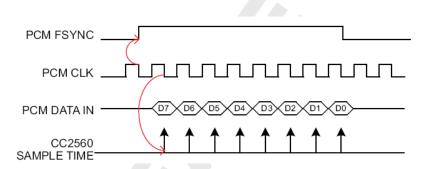


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11.4. CLOCK-EDGE OPERATION

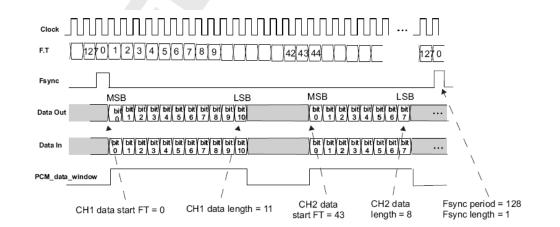
The codec interface of the CC2560 can work on the rising or the falling edge of the clock. It also has the ability to sample the frame sync and the data at inversed polarity.

This is the operation of a falling-edge-clock type of codec. The codec is the master of the PCM bus. The frame sync signal is updated (by the codec) on the falling clock edge and therefore shall be sampled (by the CC2560) on the next rising clock. The data from the codec is sampled (by the CC2560) on the clock falling edge.



11.5. TWO-CHANNEL PCM BUS EXAMPLE

In Figure 4-8, a 2-channel PCM bus is shown where the two channels have different word sizes and arbitrary positions in the bus frame. (FT stands for Frame Timer)



11.6. AUDIO ENCODING

The CC2560 codec interface can use one of four audio-coding patterns:

- A-Law (8-bit)
- µ-Law (8-bit)
- Linear (8- or 16-bit)

11.7. IMPROVED ALGORITHM FOR LOST PACKETS

The CC2560 features an improved algorithm for improving voice quality when received voice data packets are lost. There are two options:

• Repeat the last sample – possible only for sample sizes up to 24 bits. For sample sizes >24 bits, the last byte is repeated.

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• Repeat a configurable sample of 8 to 24 bits (depends on the real sample size), in order to simulate silence (or anything else) in the PCM bus. The configured sample will be written in a specific register for each channel.

The choice between those two options is configurable separately for each channel.

11.8. BLUETOOTH/PCM CLOCK MISMATCH HANDLING

In Bluetooth RX, the CC2560 receives RF voice packets and writes these to the codec I/F. If the CC2560 receives data faster than the codec I/F output allows, an overflow will occur. In this case, the Bluetooth has two possible behaviour modes: 'allow overflow' and 'don't allow overflow'.

• If overflow is allowed, the Bluetooth will continue receiving data and will overwrite any data not yet sent to the codec.

• If overflow is not allowed, RF voice packets received when buffer is full will be discarded.

11.9. BLUETOOTH INTER-IC SOUND (I2S)

The CC2560 can be configured as an Inter-IC Sound (I2S) serial interface to a I2S codec device. In this mode, the CC2560 audio codec interface is configured as a bidirectional, full-duplex interface, with two time slots per frame: Time slot 0 is used for the left channel audio data and time slot 1 for the right channel audio data. Each time slot is configurable up to 40 serial clock cycles in length and the frame is configurable up to 80 serial clock cycles in length.

11.10. CURRENT CONSUMPTION FOR DIFFERENT BLUETOOTH SCENARIOS The following table gives average current consumption for different Bluetooth scenarios.

Conditions: VDD_IN = 3.6 V, 25°C, 26-MHz fast clock, nominal unit, 4 dBm output power.

Mode Description	Master/Slave	Average Current	Unit
Idle current (ARM off)	Master/Slave	2.5	mA
SCO link HV3	Master/Slave	12	mA
eSCO link EV3 64 kbps, no retransmission	Master/Slave	11.5	mA
eSCO link 2-EV3 64 kbps, no retransmission	Master/Slave	8.3	mA
GFSK full throughput: TX = DH1, RX = DH5	Master/Slave	38.5	mA
EDR full throughput: TX = 2-DH1, RX = 2-DH5	Master/Slave	39.2	mA
EDR full throughput: TX = 3-DH1, RX = 3-DH5	Master/Slave	39.2	mA
Sniff, 1 attempt, 1.28 s	Master/Slave	76/100	μΑ
Page or Inquiry Scan 1.28 s, 11.25 ms	Master/Slave	300	μA
Page (1.28 s) and Inquiry (2.56 s) scans, 11.25 ms	Master/Slave	430	μA
Low power scan, 1.28-s interval, quiet environment	Master/Slave	135	μA

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12. BLUETOOTH RF PERFORMANCE (BT) 12.1. BLUETOOTH CHARACTERISTICS							
No	Characteristics		Тур	BT Spec Max Class1	BT Spec Min Class1		
1	Average Power Hop	oping DH5 [dBm] ^{18, 19}	7.2	20	4		
2	Average Power: Ch		7.5	20	4		
3	Peak Power: Ch0 [c		7.7	23			
4	Average Power: Ch	39 [dBm] ^{18, 19}	7.0	20	4		

No	Characteristics	Тур	BT Spec Max	BT Spec Min
			Class1	Class1
1	Average Power Hopping DH5 [dBm] 18, 19	7.2	20	4
2	Average Power: Ch0 [dBm] ^{18, 19}	7.5	20	4
3	Peak Power: Ch0 [dBm] ^{18, 19}	7.7	23	
4	Average Power: Ch39 [dBm] ^{18, 19}	7.0	20	4
5	Peak Power: Ch39 [dBm] ^{18, 19}	7.2	23	
6	Average Power: Ch78 [dBm] ^{18, 19}	6.7	20	4
7	Peak Power: Ch78 [dBm] ^{18, 19}	7.0	23	
8	Max. Frequency Tolerance: Ch0 [kHz]	-2.6	75	-75
9	Max. Frequency Tolerance: Ch39 [kHz]	-2.2	75	-75
10	Max. Frequency Tolerance: Ch78 [kHz]	-2.1	75	-75
11	Max. Drift: Ch0_DH1 [kHz]	3.6	25	-25
12	Max. Drift: Ch0_DH3 [kHz]	3.7	40	-40
13	Max. Drift: Ch0_DH5 [kHz]	4.0	40	-40
14	Max. Drift Rate: Ch0_DH1 [kHz]	-2.6	20	-20
15	Max. Drift Rate: Ch0_DH3 [kHz]	-3.2	20	-20
16	Max. Drift Rate: Ch0_DH5 [kHz]	-3.3	20	-20
17	Max. Drift: Ch39_DH1 [kHz]	4.0	25	-25
18	Max. Drift: Ch39_DH3 [kHz]	4.3	40	-40
19	Max. Drift: Ch39_DH5 [kHz]	4.3	40	-40
20	Max. Drift Rate: Ch39_DH1 [kHz]	-3.1	20	-20
21	Max. Drift Rate: Ch39_DH3 [kHz]	-3.6	20	-20
22	Max. Drift Rate: Ch39_DH5 [kHz]	-3.7	20	-20
23	Max. Drift: Ch78_DH1 [kHz]	4.1	25	-25
24	Max. Drift: Ch78_DH3 [kHz]	4.5	40	-40
25	Max. Drift: Ch78_DH5 [kHz]	4.4	40	-40
26	Max. Drift Rate: Ch78_DH1 [kHz]	-3.4	20	-20
27	Max. Drift Rate: Ch78_DH3 [kHz]	-3.9	20	-20
28	Max. Drift Rate: Ch78_DH5 [kHz]	-4.1	20	-20
29	Delta F1 Avg: Ch0 [kHz]	159.5	175	140
30	Delta F2 Max.: Ch0 [%]	100.0		99.9
31	Delta F2 Avg/Delta F1 Avg: Ch0	0.9		0.8
32	Delta F1 Avg: Ch39 [kHz]	159.8	175	140
33	Delta F2 Max.: Ch39 [%]	100.0		99.9
34	Delta F2 Avg/Delta F1 Avg: Ch39	0.9		0.8
35	Delta F1 Avg: Ch78 [kHz]	159.1	175	140
36	Delta F2 Max.: Ch78 [%]	100.0		99.9
37	Delta F2 Avg/Delta F1 Avg: Ch78	0.9		0.8
45	Sensitivity	-93.0		-81
46	f(H)-f(L): Ch0 [kHz]	918.4	1000	
47	f(H)-f(L): Ch39 [kHz]	918.3	1000	

SUBJECT			PRODUCT SPECIFICATION			CATION No. DS-1315-2400-7			RE\ 1.04
CLASS 1			or 2 BLUETOOTH MODULE			TH MODULE PAGE			41
CUSTOMER'S CODE PAN1315			ANASONIC'S C NW89818C2JF			DAT	Ξ	02.07.20	011
-						BT Spec	BT Spec		
N	lo Characteris	stics			Тур	Max	Min		
		70 (111.)				Class1	Class1		
	8 f(H)-f(L): Cl		1		918.2	1000			
4		<u>3: Ch3 [dBm</u> 2: Ch3 [dBm			-51.5 -50.4	-40 -40			
5		2: Ch3 [dBm 1: Ch3 [dBm			-50.4	-40			
5		Center: Ch3			8.1	20	4		
5		-1: Ch3 [dBn			-19.2	20	4		
5		-2: Ch3 [dBn			-50.7	-40			
5		-3: Ch3 [dBn			-53.3	-40			
5		3: Ch39 [dBr			-51.6	-40			
5		2: Ch39 [dBi 2: Ch39 [dBi			-50.7	-40			
5		1: Ch39 [dBr	1		-19.0	-40			
5		Center: Ch39			7.7	20	4		
6		-1: Ch39 [dB			-19.7	20	4		
6		-2: Ch39 [dB			-50.9	-40			
6		-3: Ch39 [dB			-53.2	-40			
6		3: Ch75 [dBr			-51.7	-40			
6		2: Ch75 [dBi 2: Ch75 [dBi			-50.7	-40			
6		1: Ch75 [dBr			-19.2	-40			
6		Center: Ch75			7.5	20	4		
6		-1: Ch75 [dB			-20.0	20	4		
6		-2: Ch75 [dB			-51.0	-40			
6		-3: Ch75 [dB	-		-53.4	-40			
7		DH5: Ch0 [kl			-4.7	75	-75		
7			H5: Ch0 [kHz]		-6.0	75	-75		
		DH5: Ch0 [k			-0.0	10	-10		
		S 2-DH5: Ch			0.0	0.2	-10		
		k 2-DH5: Ch			0.0	0.35			
		6 2-DH5: Ch			100.0	0.35	99		
		DH5: Ch0 [kl			-3.7	75	-75		
7			H5: Ch0 [kHz]		-5.8	75	-75		
		DH5: Ch0 [k			-2.6	10	-10		
		S 3-DH5: Ch	•		0.0	0.13	-10		
		k 3-DH5: Ch			0.0	0.25			
8		6 3-DH5: Ch			100.0	0.20	99		
		DH5: Ch39 [-4.8	75	-75		
			H5: Ch39 [kHz]		-6.1	75	-75		
	ÿ	DH5: Ch39			-1.4	10	-10		
		S 2-DH5: Ch			0.0	0.2			
		k 2-DH5: Ch			0.0	0.35			
		6 2-DH5: Ch	• •		100.0	0.00	99		
		DH5: Ch39 [-3.8	75	-75		
			H5: Ch39 [kHz]		-5.9	75	-75		
		DH5: Ch39			-2.6	10	-10		
9	Ŭ	S 3-DH5: Ch			0.0	0.13			
3	2 DEVM Rea				0.0	0.25	+		

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No	Characteristics	Тур	BT Spec Max	BT Spec Min
			Class1	Class1
93	DEVM 99% 3-DH5: Ch39 [%]	100.0		99
94	omega i 2-DH5: Ch78 [kHz]	-4.9	75	-75
95	omega o + omega i 2-DH5: Ch78 [kHz]	-6.2	75	-75
96	omega o 2-DH5: Ch78 [kHz]	-1.4	10	-10
97	DEVM RMS 2-DH5: Ch78 [%]	0.0	0.2	
98	DEVM Peak 2-DH5: Ch78 [%]	0.1	0.35	
99	DEVM 99% 2-DH5: Ch78 [%]	100.0		99
100	omega i 3-DH5: Ch78 [kHz]	-3.8	75	-75
101	omega o + omega i 3-DH5: Ch78 [kHz]	-6.0	75	-75
102	omega o 3-DH5: Ch78 [kHz]	-2.7	10	-10
103	DEVM RMS 3-DH5: Ch78 [%]	0.0	0.13	
104	DEVM Peak 3-DH5: Ch78 [%]	0.1	0.25	
105	DEVM 99% 3-DH5: Ch78 [%]	100.0		99

No	Characteristics	Condition	Min	Тур	Max	BT Spec	Unit
1	Operation frequency range		2402		2480		MHz
2	Channel spacing			1			MHz
3	Input impedance			50			Ω
		GFSK, BER = 0.1%		-93.0		-70	
4	Sensitivity, Dirty Tx on	Pi/4-DQPSK, BER = 0.01%		-92.5		-70	dBm
		8DPSK, BER = 0.01%		-85.5		-70	

No	Characteristics	Condition	Тур	Max	Unit		
1	Tx and Rx out-of-band emissions	30 kHz to 1 GHz ¹⁷ , ¹⁸ , ¹⁹		-30	dBm		
1	Output signal = 7dBm	1 to 12.75 GHz ^{17, 18, 19}		-30	авт		
2	2nd harmonic	at 7dBm output power ^{17, 18, 19}		-30	dBm		
3	3rd harmonic	at 7dBm output power ^{17, 18, 19}		-30	dBm		

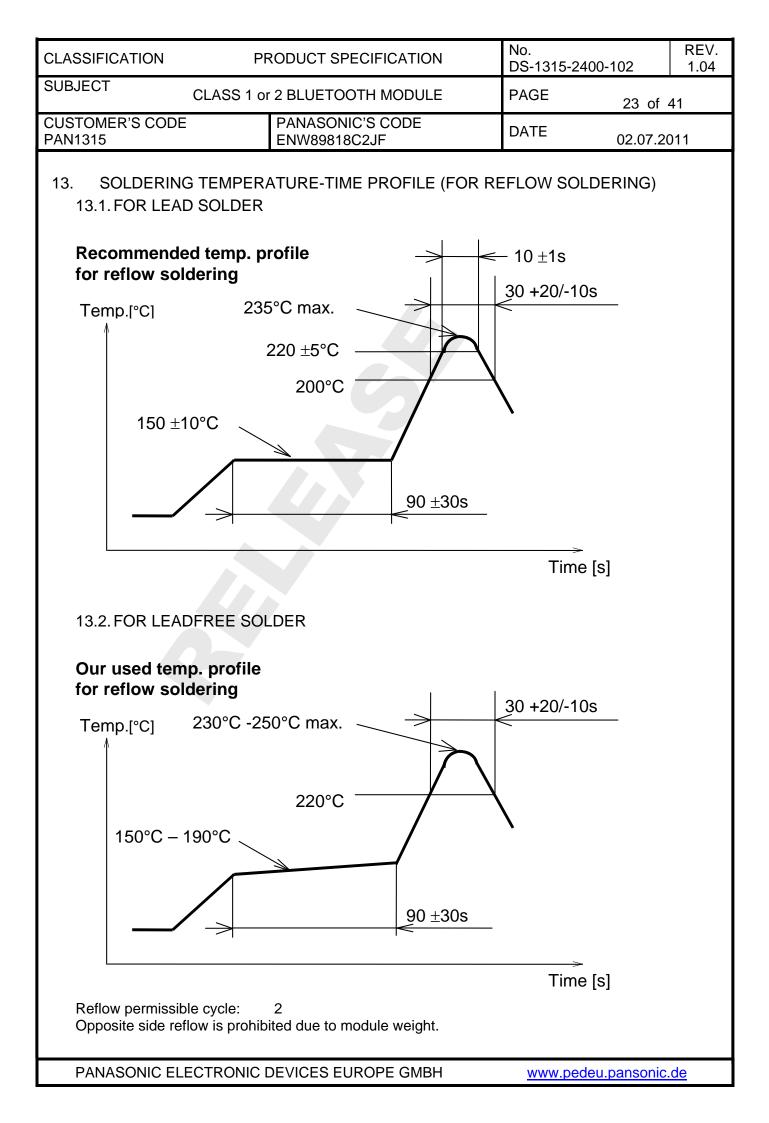
The values are measured conducted. Therefore we expect even better suppression of the spurious emissions at customer application with antenna. Usual antennas have band pass filter characteristics.

By using a different filter design, Panasonic is working active on an improved pin and function compatible module, to comply with the out of band emissions also with higher output power (+10dBm). This will NOT affect the FCC ID, please refer to chapter 29.1.

¹⁷ Includes effects of frequency hopping

¹⁸ Average according FCC, IC and ETSI requirements. Above +7dBm output power (refer also to 19) the customer has to verify the final product against national regulations.

¹⁹ +7dBm related to power register value 18, according to TI service pack 2.30



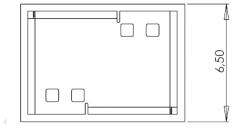
CLASSIFICATION	PRO	ODUCT SPECIFICATION	No. DS-1315-2400-	102	REV. 1.04
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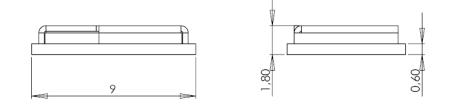
14. MODULE DIMENSION

No.	Item	Dimension	Tolerance	Remark
1	Width	6.50	± 0.20	
2	Lenght	9.00	± 0.20	
3	Height	1.80	± 0.20	With case

PAN1315 Module Drawing







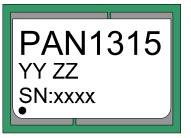
CLASSIFICATION	No. DS-1315-2400-1	REV.	
SUBJECT CL	PAGE	25 of 41	
CUSTOMER'S CODE PAN1315	PANASONIC'S CODE ENW89818C2JF	DATE	02.07.2011
15. PAN1315 FOO	TPRINT OF THE MODULE		
All dimensions are ir The outer dimension	m millimeters. In the matrix of the matrix		
The layout is symetric center by 1mm.	ic to center. The inner pins (2,4,6,9,11,14,16	,18,21,23) are shifi	ted to the
Please note that the	ES samples with HW Revision 01 have 0.5n	nm pads!	
	3.95 2.95 1.80 0.90+		
ے بر	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		

9,00

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16. LABELING DRAWING

16.1. ENGINEERING SAMPLES



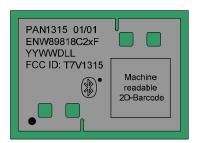
Above white carbon label is suitable for reflow soldering and designed for the engineering sample status.

The point on the label (below left) is the identifier for pin 1 of the module.

As a summary:

Print	Description	Description		
XXXX	Serial Number	Serial Number		
YY	Hardware Ider	Hardware Identifier:		
	EE	EE With EEPROM		
	00 Without EEPROM			
ZZ	Software Identifier:			
	01	first identifier for the software version		

16.2. MASS PRODUCTION



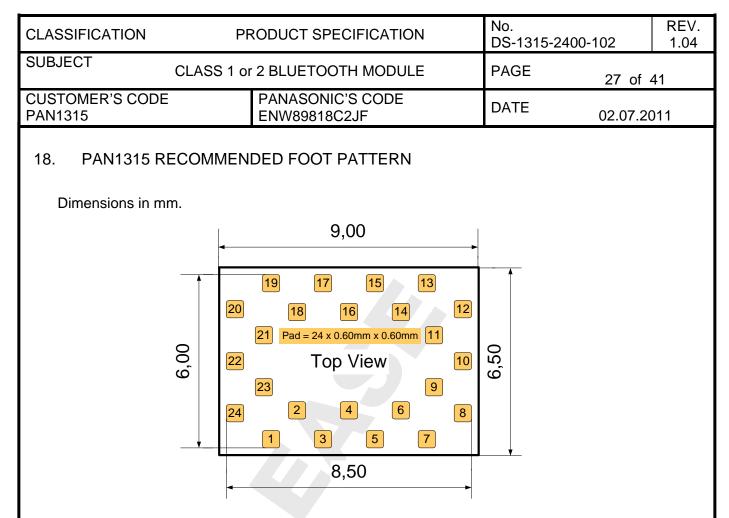
This will be a laser marking on the top case, drawing is only an example.

17. MECHANICAL REQUIREMENTS

No.	Item	Limit	Condition
1	Solderability	More than 75% of the soldering area shall be coated by solder	Reflow soldering with recommendable temperature profile
2	Resistance to soldering heat	It shall be satisfied electrical requirements and not be mechanical damage	See chapter 13.2

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The land pattern dimensions above are meant to serve only as a guide. This information is provided without any legal liability.

For the solder paste screen, use the same screen for the module. Solder paste screen cutouts (with slightly different dimensions) might be optimum depending on your soldering process. For example, the solder paste screen thickness chosen might have an effect. The solder screen thickness depends on your production standard -- $120\mu m$ to $150\mu m$ is recommended.

IMPORTANT:

Although the bottom side of PAN1315 is fully coated, no copper such as through hole vias, planes or tracks on the board component layer should be located below the PAN1315 to avoid creating a short. In cases where a track or through hole via has to be located under the module, please make a note that it has to be kept away from PAN1315 bottom pads. The PAN1315 multilayer pcb contains an inner RF shielding plane, therefore no pcb shielding plane below the module is needed.

When using an onboard ceramic antenna, please place the antenna on the edge of your carrier board (if allowable).

If you have any questions on these points, please contact your local Panasonic representative.

Before releasing the layout, we recommend to sent the schematic and layout for final check to <u>wireless@eu.panasonic.com</u>.

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19. DEVELOPMENT OF AF	PLICATIONS		
and Panasonics PAN1315. A	Bluetooth software, including some p detailed documentation will be avai cuments, Chapter 25. Also refer to		of
19.1. TOOLS TO BE NEED MSP-EXP430F5438 - Ex	ED operimenter Board, which can be orc	dered here:	
http://focus.ti.com/doo	cs/toolsw/folders/print/msp-exp430f5	5438.html	
	ebugging Interface, which can be or cs/toolsw/folders/print/msp-fet430uif		
PAN1315EMK - Bluetoot	th Evaluation Module Kit for MSP430		
here: TI link:			
http://focus.ti.com/docs <u>T+pan1315emk</u> Panasonic link:	stoolsw/folders/print/pan1315emk.html? com/industrial/electronic-components/rf-		<u>M+O</u>
modules/bluetooth/pan			
		ASPRAJO	
MSP-EXP430F5438		ши 875 Въуникаонта саци, в сиорантека Са 1. 1876 Въуникаонта саци, в сиорантека Саци 1. 1876 Въуникаонта саци, в сиорантека Саци, в сиорантека Саци, в с	8 1
MSP-EXP430F5438 MSP430F5438 Exper	rimenter Board	DANAGASETU	
		PAN1315ETU	
· · · · · · · · · · · · · · · · · · ·	software development environment, to Fehler! Verweisquelle konnte r		
	n of the usage for the tools refer to:		
http://wiki.msp430.com/inde	ex.php/MSP430_Bluetooth_Platform		
	ules are available through Panasonic al information visit <u>www.panasonic.c</u>		

CLASSIFICATION	PR	ODUCT SPECIFICATION	No. DS-1315-2400-102	REV. 1.04
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20. RELIABILITY TESTS

The measurement should be done after being exposed to room temperature and humidity for 1 hour.

No.	Item	Limit	Condition
1	Vibration test	Electrical parameter should be in specification	 a) Freq.:10~50Hz,Amplitude:1.5mm a) 20min. / cycle,1hrs. each of XYZ axis b) Freq.:30~100Hz, 6G b) 20min. / cycle,1hrs. each of XYZ axis
2	Shock test	the same as above	Dropped onto hard wood from height of 50cm for 3 times
3	Heat cycle test	the same as above	-40°C for 30min. and +85°C for 30min.; each temperature 300 cycles
4	Moisture test	the same as above	+60°C, 90% RH, 300h
5	Low temp. test	the same as above	-40°C, 300h
6	High temp. test	the same as above	+85°C, 300h

21. CAUTIONS

Failure to follow the guidelines set forth in this document may result in degrading of the product's functions and damage to the product.

21.1. DESIGN NOTES

- (1) Follow the conditions written in this specification, especially the control signals of this module.
- (2) The supply voltage has to be free of AC ripple voltage (for example from a battery or a low noise regulator output). For noisy supply voltages, provide a decoupling circuit (for example a ferrite in series connection and a bypass capacitor to ground of at least 47uF directly at the module).
- (3) This product should not be mechanically stressed when installed.
- (4) Keep this product away from heat. Heat is the major cause of decreasing the life of these products.
- (5) Avoid assembly and use of the target equipment in conditions where the products' temperature may exceed the maximum tolerance.
- (6) The supply voltage should not be exceedingly high or reversed. It should not carry noise and/or spikes.
- (7) Keep this product away from other high frequency circuits.

21.2. INSTALLATION NOTES

- Reflow soldering is possible twice based on the conditions in chapter 15. Set up the temperature at the soldering portion of this product according to this reflow profile.
- (2) Carefully position the products so that their heat will not burn into printed circuit boards or affect the other components that are susceptible to heat.
- (3) Carefully locate these products so that their temperatures will not increase

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(4) \ k	If a vinyl-covered w will melt and gener between the cover	of heat generated by neighboring co vire comes into contact with the pro rate toxic gas, damaging the insulat and these products to occur. d not be mechanically stressed or v	oducts, then the cover tion. Never allow contact	

- (6) If you want to repair your board by hand soldering, please keep the conditions of this chapter.
- (7) Do not wash this product.
- (8) Refer to the recommended pattern when designing a board.
- (9) Pressing on parts of the metal cover or fastening objects to the metal will cause damage to the unit.

21.3. USAGE CONDITIONS NOTES

- (1) Take measures to protect the unit against static electricity. If pulses or other transient loads (a large load applied in a short time) are applied to the products, check and evaluate their operation befor assembly on the final products.
- (2) Do not use dropped products.
- (3) Do not touch, damage or soil the pins.
- (4) Follow the recommended condition ratings about the power supply applied to this product.
- (5) Electrode peeling strength: Do not add pressure of more than 4.9N when soldered on PCB.
- (6) Pressing on parts of the metal cover or fastening objects to the metal cover will cause damage.
- (7) These products are intended for general purpose and standard use in general electronic equipment, such as home appliances, office equipment, information and communication equipment.

21.4. STORAGE NOTES

- (1) The module should not be stressed mechanically during storage.
- (2) Do not store these products in the following conditions or the performance characteristics of the product, such as RF performance will be adversely affected:
 - Storage in salty air or in an environment with a high concentration of corrosive gas, such as Cl2, H2S, NH3, SO2, or NOX
 - Storage in direct sunlight
 - Storage in an environment where the temperature may be outside the range of 5°C to 35°C range, or where the humidity may be outside the 45 to 85% range.
 - Storage of the products for more than one year after the date of delivery Storage period: Please check the adhesive strength of the embossed tape and soldering after 6 months of storage.
- (3) Keep this product away from water, poisonous gas and corrosive gas.
- (4) This product should not be stressed or shocked when transported.
- (5) Follow the specification when stacking packed crates (max. 10).

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21.5. SAFETY CAUTIONS

These specifications are intended to preserve the quality assurance of products and individual components.

Before use, check and evaluate the operation when mounted on your products. Abide by these specifications, without deviation when using the products. These products may short-circuit. If electrical shocks, smoke, fire, and/or accidents involving human life are anticipated when a short circuit occurs, then provide the following failsafe functions, as a minimum.

- (1) Ensure the safety of the whole system by installing a protection circuit and a protection device.
- (2) Ensure the safety of the whole system by installing a redundant circuit or another system to prevent a single fault causing an unsafe status.

21.6. OTHER CAUTIONS

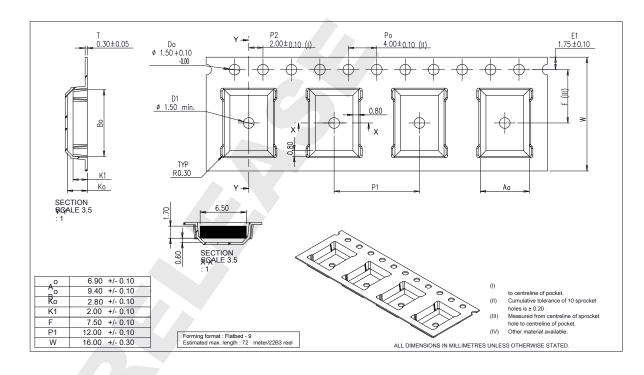
- (1) This specification sheet is copyrighted. Please do not disclose it to a third party.
- (2) Please do not use the products for other purposes than those listed.
- (3) Be sure to provide an appropriate fail-safe function on your product to prevent an additional damage that may be caused by the abnormal function or the failure of the product.
- (4) This product has been manufactured without any ozone chemical controlled under the Montreal Protocol.
- (5) These products are not intended for other uses, other than under the special conditions shown below. Before using these products under such special conditions, check their performance and reliability under the said special conditions carefully to determine whether or not they can be used in such a manner.
 - In liquid, such as water, salt water, oil, alkali, or organic solvent, or in places where liquid may splash.
 - In direct sunlight, outdoors, or in a dusty environment
 - In an environment where condensation occurs.
 - In an environment with a high concentration of harmful gas (e.g. salty air, HCI, CI2, SO2, H2S, NH3, and NOX)
- (6) If an abnormal voltage is applied due to a problem occurring in other components or circuits, replace these products with new products because they may not be able to provide normal performance even if their electronic characteristics and appearances appear satisfactory.
- (7) When you have any question or uncertainty, contact Panasonic.

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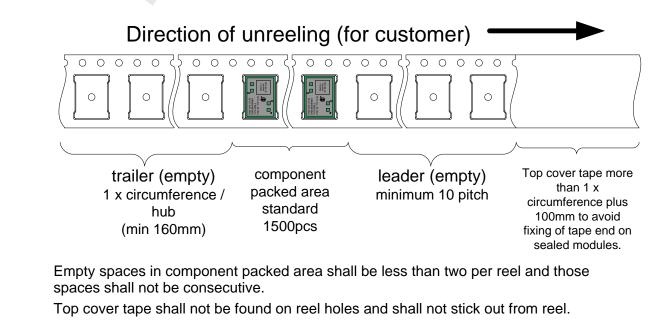
22. PACKAGING

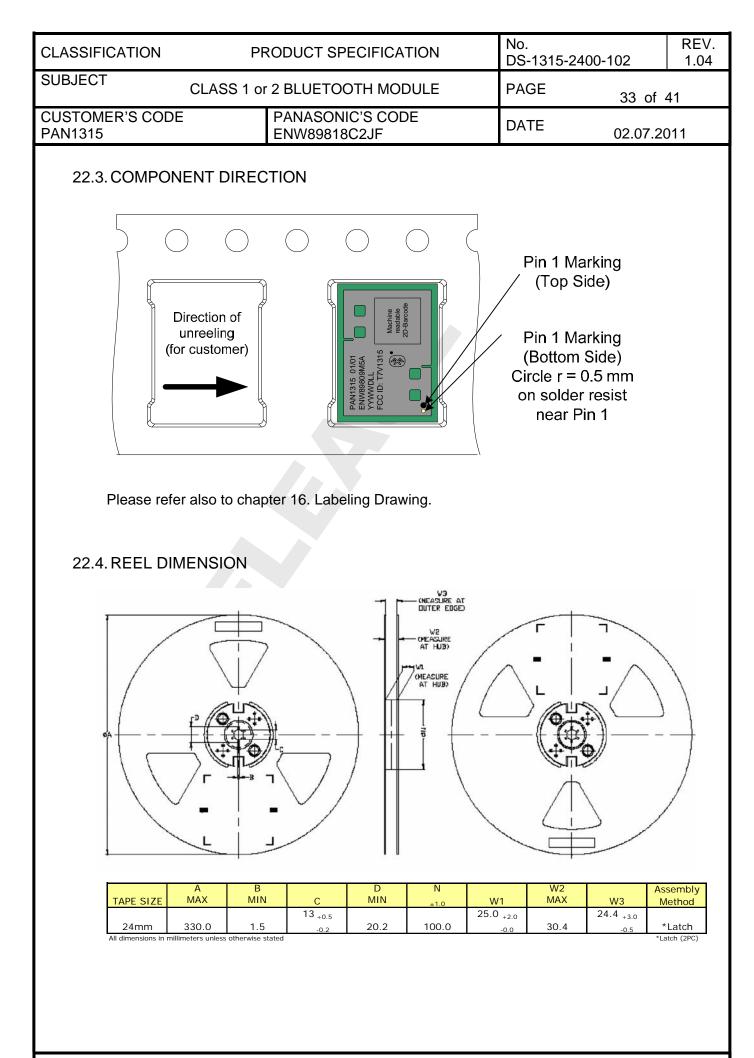
If the product has mass production status, indicated in chapter 25, we will deliver the module in the package which are described below.

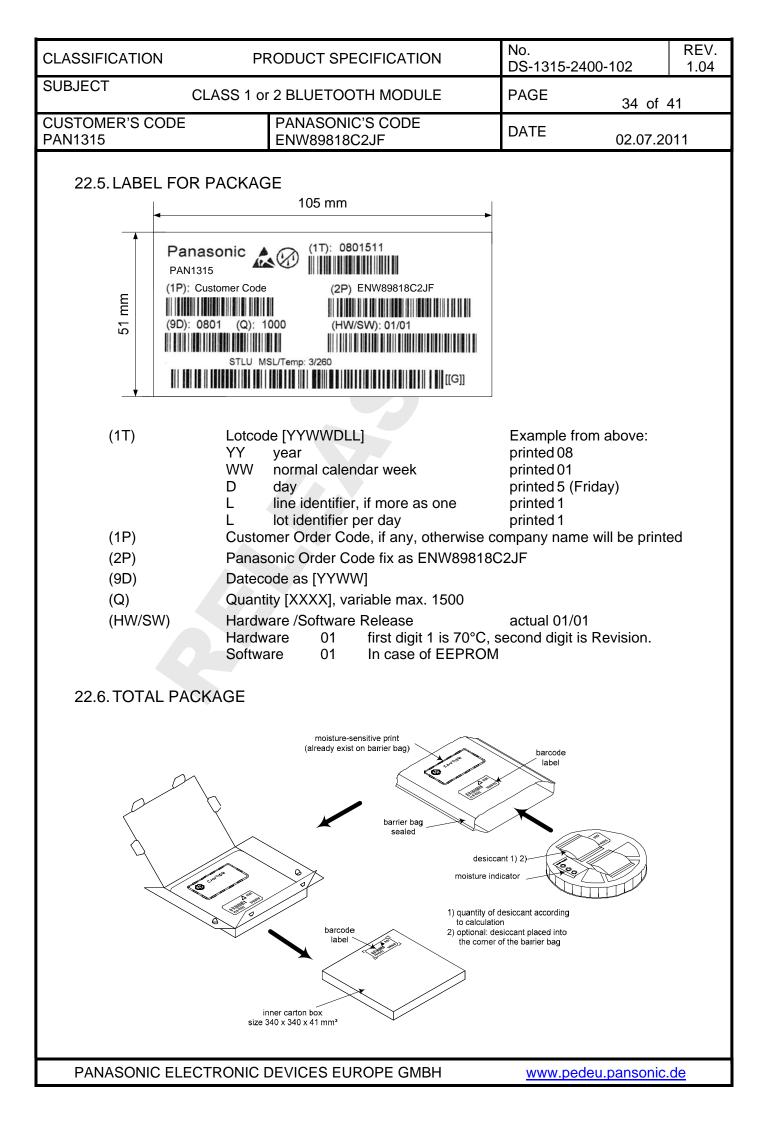
22.1. TAPE DIMENSION



22.2. PACKING IN TAPE







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23. ORDERING INFORMATION

Ordering part number	Description	MOQ ⁽¹⁾
ENW89818C2JF (2)	PAN1315 CLASS 2 Bluetooth HCI Module (Standard 2.1),without ceramic antenna, 50 Ω bottom pad, operating temperature 70°C.	1500
ENW89818A2JF ⁽³⁾	PAN1325 CLASS 2 Bluetooth HCI Module (Standard 2.1), with ceramic antenna, operating temperature 70°C. Refer to AN-1325-2420-111.	1500

Notes:

- (1) Abbreviation for Minimum Order Quantity (MOQ). The standard MOQ for mass production are 1500 pieces, fewer only on customer demand. Samples for evaluation can be delivered at any quantity.
- (2) Samples available
- (3) Samples will be available on customer demand

24. ROHS DECLARATION

Declaration of environmental compatibility for supplied products:

Hereby we declare to our best present knowledge based on declaration of our suppliers that this product do not contain by now the following substances which are banned by Directive 2002/95/EC (RoHS) or if contain a maximum concentration of 0,1% by weight in homogeneous materials for

- Lead and lead compounds
- Mercury and mercury compounds
- Chromium (VI)
- PBB (polybrominated biphenyl) category
- PBDE (polybrominated biphenyl ether) category

And a maximum concentration of 0,01% by weight in homogeneous materials for

• Cadmium and cadmium compounds

25. DATA SHEET STATUS

This data sheet contains the final specification (RELEASE).

Panasonic reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

Supplementary data will be published at a later date.

Please consult the most recently issued data sheet before initiating or completing a design.

If there is an update, please download under: PAN1315 Latest Data Sheet!

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26. HISTORY FOR THIS DOCUMENT

Revision	Date	Modification / Remarks
0.90	18.12.2009	1 st preliminary version
0.95	01.03.2010	Updated chapter 15 and 23.
0.96	Not released	Change ESD Information on foot note 5 (page 6)
0.97	25.03.2010	Various updates. Deleted links to TI Datasheet.
0.98	21.04.2010	Updated Links Some minor changes in chapter 8 and 9.1 and change the base for the values in chapter 9.
0.99	22.10.2010	Adopted changes according to CC2560 Datasheet. Included Interface Description, performance values. Not released.
1.00	04.11.2010	1 st internal Release.
1.01	03.12.2010	Included reference to PAN1325 Application Note. AN-1325-2420-111.pdf
1.02	10.01.2011	Changed wording in chapter 30 "Industry Canada Certification".
1.03	23.05.2011	Included DOC for PAN1315 series. Included PAN13xx ANT and BLE Addendum Rev1.x.pdf reference. Included Note for IO voltage and MLD_OUT pin.
1.04	02.07.2011	Corrected wording in chapter 31 European R&TTE Declaration of Conformity.

27. RELATED DOCUMENTS

For an update, please search in the suitable homepage.

- [1] PAN1315ETU Design-Guide: http://www.panasonic.com/industrial/includes/pdf/PAN1315ETU_design-guide.pdf
- [2] CC2560 Product Bulletin: <u>http://focus.ti.com/pdfs/wtbu/cc2560_slyt377.pdf</u>
- [3] Bluetooth SW for MSP430 is supported by IAR IDE service pack 5.10.6 and later. You must use IAR full version edition (you cannot use kick-start version). You can find info on IAR at http://www.iar.com/website1/1.0.1.0/3/1/ and www.MSP430.com. Please note, that there is an option for a 30-days free version of IAR evaluation edition.
- [4] AN-1325-2420-111. Application Note for PAN1325 Antenna Version.
- [5] PAN13xx ANT and BLE Addendum Rev1.x.pdf

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28. GENERAL INFORMATION

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All rights reserved.

This product description does not lodge the claim to be complete and free of mistakes. Please contact the related product manager in every case.

If we deliver ES samples to the customer, these samples have the status Engineering Samples. This means, the design of this product is not yet concluded. Engineering Samples may be partially or fully functional, and there may be differences to be published Data Sheet.

Engineering Samples are not qualified and are not to be used for reliability testing or series production.

Disclaimer:

Customer acknowledges that samples may deviate from the Data Sheet and may bear defects due to their status of development and the lack of qualification mentioned above. Panasonic rejects any liability or product warranty for Engineering Samples. In particular, Panasonic disclaims liability for damages caused by

- the use of the Engineering Sample other than for Evaluation Purposes, particularly the installation or integration in an other product to be sold by Customer,
- deviation or lapse in function of Engineering Sample,
- improper use of Engineering Samples.

Panasonic disclaimes any liability for consequential and incidental damages.

In case of any questions, please contact your local sales partner or the related product manager.

29. REGULATORY INFORMATION

29.1. FCC NOTICE



The device PAN1325, including the ceramic antenna (ENW89818A2JF) and also the SMD type PAN1315 (ENW89818C2JF), including with the antennas, which are listed in 29.5, complies with Part 15 of the FCC Rules. The device meets the requirements for modular transmitter approval as detailed in FCC public Notice DA00-1407.transmitter Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) This device must accept any interference received, including interference that may cause undesired operation.

29.2. CAUTION



The FCC requires the user to be notified that any changes or modifications made to this device that are not expressly approved by Panasonic Electronic Devices Europe GmbH may void the user's authority to operate the equipment.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio

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communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help

29.3. LABELING REQUIREMENTS



The Original Equipment Manufacturer (OEM) must ensure that FCC labeling requirements are met. This includes a clearly visible label on the outside of the OEM enclosure specifying the appropriate Panasonic FCC identifier for this product as well as the FCC Notice above. The FCC identifier are **FCC ID: T7V1315**. This FCC identifier is valid for both versions (PAN1315 and PAN1325), for details, please see the chapter 23. Ordering Information.

In any case the end product must be labelled exterior with "Contains FCC ID: T7V1315"

29.4. ANTENNA WARNING

The related part number for this device is ENW89818C2JF (PAN1315 with SMD pad). For details, please see the chapter 23. Ordering Information. This device is tested with a standard SMA connector and with the antennas listed below. When integrated in the OEMs product, these fixed antennas require installation preventing end-users from replacing them with non-approved antennas. Any antenna not in the following table must be tested to comply with FCC Section 15.203 for unique antenna connectors and Section 15.247 for emissions. The FCC identifier for this device with the antenna listed in item 1 are the same (FCC ID: T7V1315).

29.5. APPROVED ANTENNA LIST

Note: We are able to qualify your antenna and will add to this list as that process is completed.

Item	Part Number	Manufacturer	Frequency Band	Туре	Gain (dBi)
1	2450AT43B100	Johanson Technologies	2.4GHz	Chip-Antenna	+1.3
2	LDA212G3110K	Murata	2.4GHz	Chip-Antenna	+0.9
3	4788930245	Würth Elektronik	2.4GHz	Chip-Antenna	+0.5

29.6. RF EXPOSURE PAN1315

To comply with FCC RF Exposure requirements, the Original Equipment Manufacturer (OEM) must ensure that the approved antenna in the previous table must be installed.

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The preceding statement must be included as a CAUTION statement in manuals for products operating with the approved antennas in the previous table to alert users on FCC RF Exposure compliance.

Any notification to the end user of installation or removal instructions about the integrated radio module is not allowed.

The radiated output power of PAN1315 with mounted ceramic antenna (FCC ID: T7V1315) is far below the FCC radio frequency exposure limits. Nevertheless, the PAN1315 shall be used in such a manner that the potential for human contact during normal operation is minimized.

End users may not be provided with the module installation instructions. OEM integrators and end users must be provided with transmitter operating conditions for satisfying RF exposure compliance.

30. INDUSTRY CANADA CERTIFICATION

PAN1315 is licensed to meet the regulatory requirements of Industry Canada (IC), license: IC: 216Q-1315

Manufacturers of mobile, fixed or portable devices incorporating this module are advised to clarify any regulatory questions and ensure compliance for SAR and/or RF exposure limits. Users can obtain Canadian information on RF exposure and compliance from www.ic.gc.ca.

This device has been designed to operate with the antennas listed in Table 20 above, having a maximum gain of 1.3 dBi. Antennas not included in this list or having a gain greater than 1.3 dBi are strictly prohibited for use with this device. The required antenna impedance is 50 ohms. The antenna used for this transmitter must not be co-located or operating in conjunction with any other antenna or transmitter. due to the model size the IC identifier is displayed in the installation instruction.

31. EUROPEAN R&TTE DECLARATION OF CONFORMITY

Hereby, Panasonic Electronic Devices Europe GmbH, declares that the Bluetooth module PAN1315 and their versions is in compliance with the essential requirements and other relevant provisions of Directive 1999/5/EC. As a result of the conformity assessment procedure described in Annex III of the Directive 1999/5/EC, the end-customer equipment should be labelled as follows:

C€

PAN1315 and their versions in the specified reference design can be used in the following countries: Austria, Belgium, Cyprus, Czech Republic, Denmark, Estonia, Finland, France, Germany, Greece, Hungary, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, Poland, Portugal, Slovakia, Slovenia, Spain, Sweden, The Netherlands, the United Kingdom, Switzerland, and Norway.

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	We, <u>Panasonic Electro</u> <u>High Frequency</u> <u>Zeppelinstrasse 1</u> declare under our sole re Type of equipment: <u></u> Brand name: <u></u> Model name: <u></u> to which this declaration	ation of Conformity 1999/5/EC onic Devices Europe GmbH Products Business Group 19, 21337 Lueneburg, Germany responsibility that the product: Bluetooth Module PAN1315, PAN1316, PAN1325, PAN1326 ENW89818C2JF, ENW89823C2JF ENW89818A2JF, ENW89823A2JF relates, is in compliance with all the app provisions of the European Council Dire	y (DoC)	
	Product compliance has l	Radio and Telecommunications Terminal Ec ent procedure used for this declaration is been demonstrated on the basis of:		
	- EN 60950-1: 2006 - EN 50371: 2002 - EN 301 489-1 V1.8.1 - EN 301 489-17 V2.1.1	For article 3.1 (a) : Health and Safe For article 3.1 (b) : Electromagnetic		
	- EN 300 228 V1.7.1	For article 3.2 : Effective use of spo	ectrum allocated	
		n file is kept available at: ces Europe GmbH, Zeppelinstrasse 19, 2133	37 Lueneburg, Germany	
	Issued on: Signed by the manufactu	04 th of November 2010		
	(Company name)	Panasonic Electronic Devices	Europe GmbH	
	(Signature) (Printed name) (Title)	Heino Kaehler Manager Wireless Modules	Panasonic Electronic Devices Europe GmbH Schnology Center Module Business Zeppelinstraße 19 D - 21/337 Lüneburg Tel. +45 (0)41/317 899 - 304	
	(1100)			

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32. LIFE SUPPORT POLICY

This Panasonic product is not designed for use in life support appliances, devices, or systems where malfunction can reasonably be expected to result in a significant personal injury to the user, or as a critical component in any life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness. Panasonic customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panasonic for any damages resulting.